FIG. 1

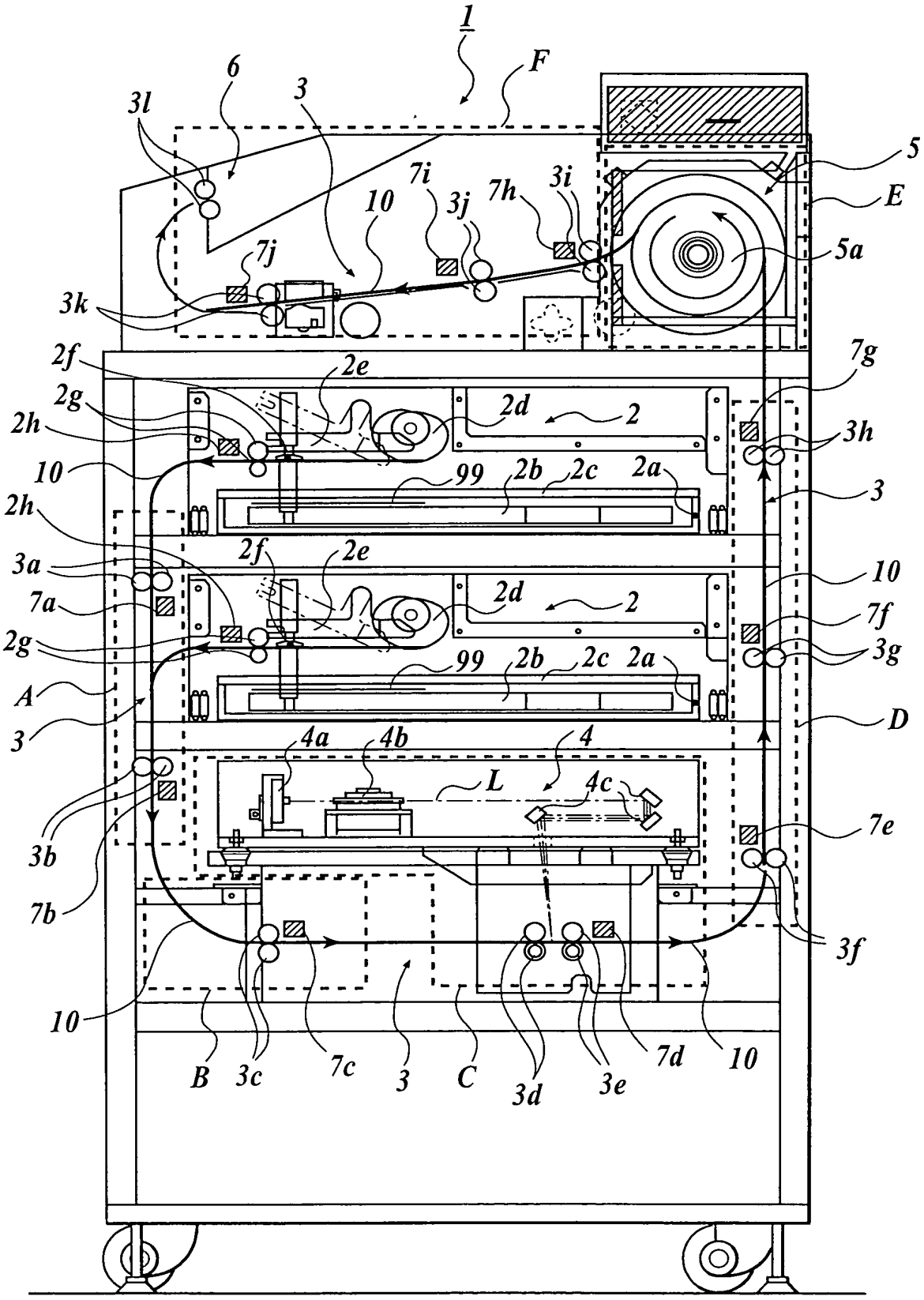
1
F
6
3
3l
5
E
7i
10
3j
7h
3i
5a
7j
3k
2f
2e
7g
2g
2d
2
2h
3h
10
99
2b
2c
2a
3
2h
2f
2e
3a
10
2d
2
7a
7f
2g
99
2b
2c
2a
3g
A
D
3
4a
4b
4
L
4c
7e
3b
7b
3f
10
B
3c
7c
3
C
3d
3e
7d
10

FIG.2

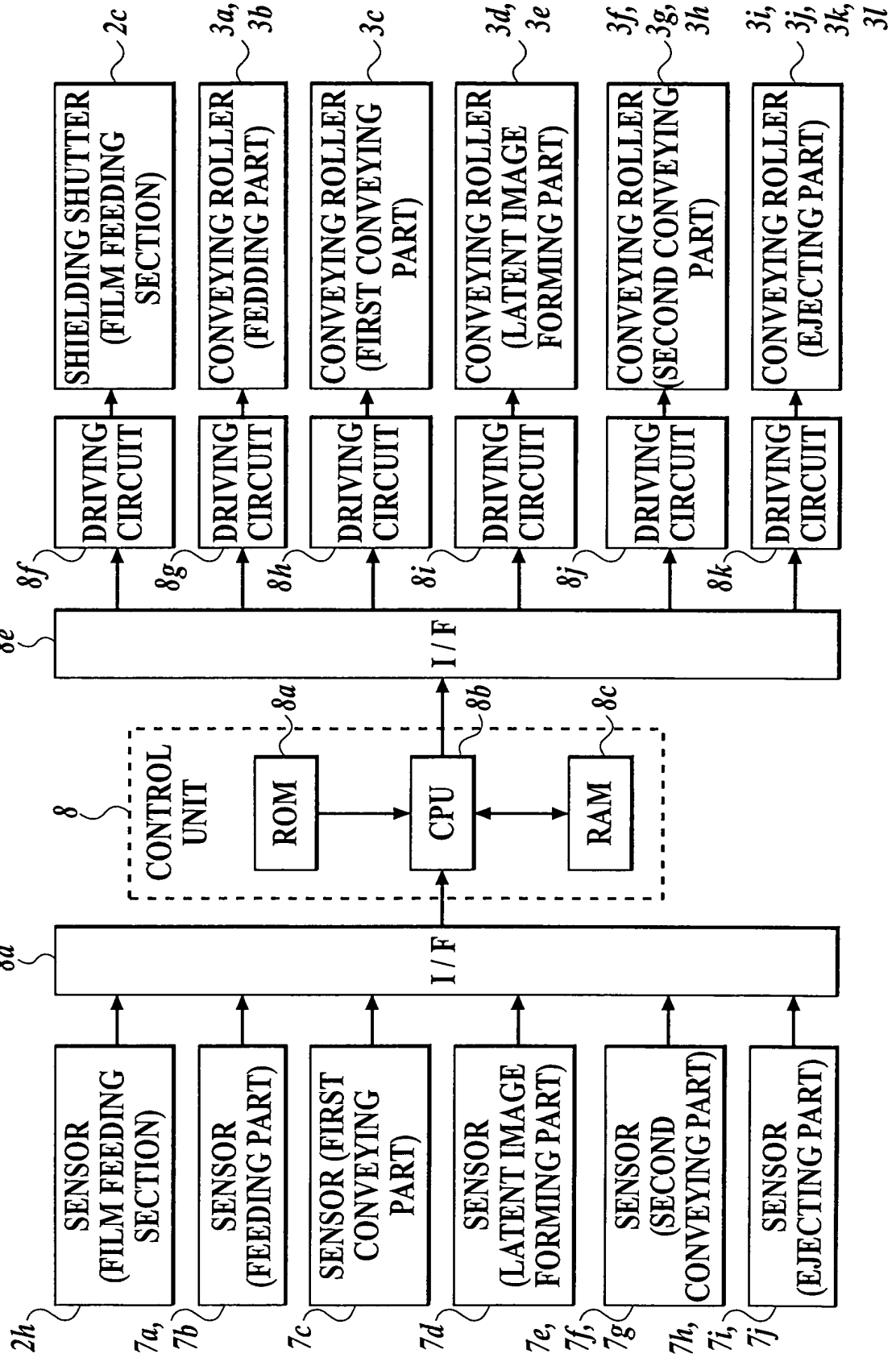
SENSOR (FILM FEEDING SECTION)
SENSOR (FEEDING PART)
SENSOR (FIRST CONVEYING PART)
SENSOR (LATENT IMAGE FORMING PART)
SENSOR (SECOND CONVEYING PART)
SENSOR (EJECTING PART)
2h
7a, 7b
7c
7d
7e, 7f, 7g
7h, 7i, 7j
8d
I / F
8
CONTROL UNIT
ROM
CPU
RAM
8a
8b
8c
8e
I / F
8f
8g
8h
8i
8j
8k
DRIVING CIRCUIT
SHIELDING SHUTTER (FILM FEEDING SECTION)
CONVEYING ROLLER (FEDDING PART)
CONVEYING ROLLER (FIRST CONVEYING PART)
CONVEYING ROLLER (LATENT IMAGE FORMING PART)
CONVEYING ROLLER (SECOND CONVEYING PART)
CONVEYING ROLLER (EJECTING PART)
2c
3a, 3b
3c
3d, 3e
3f, 3g, 3h
3i, 3j, 3k, 3l

FIG.3

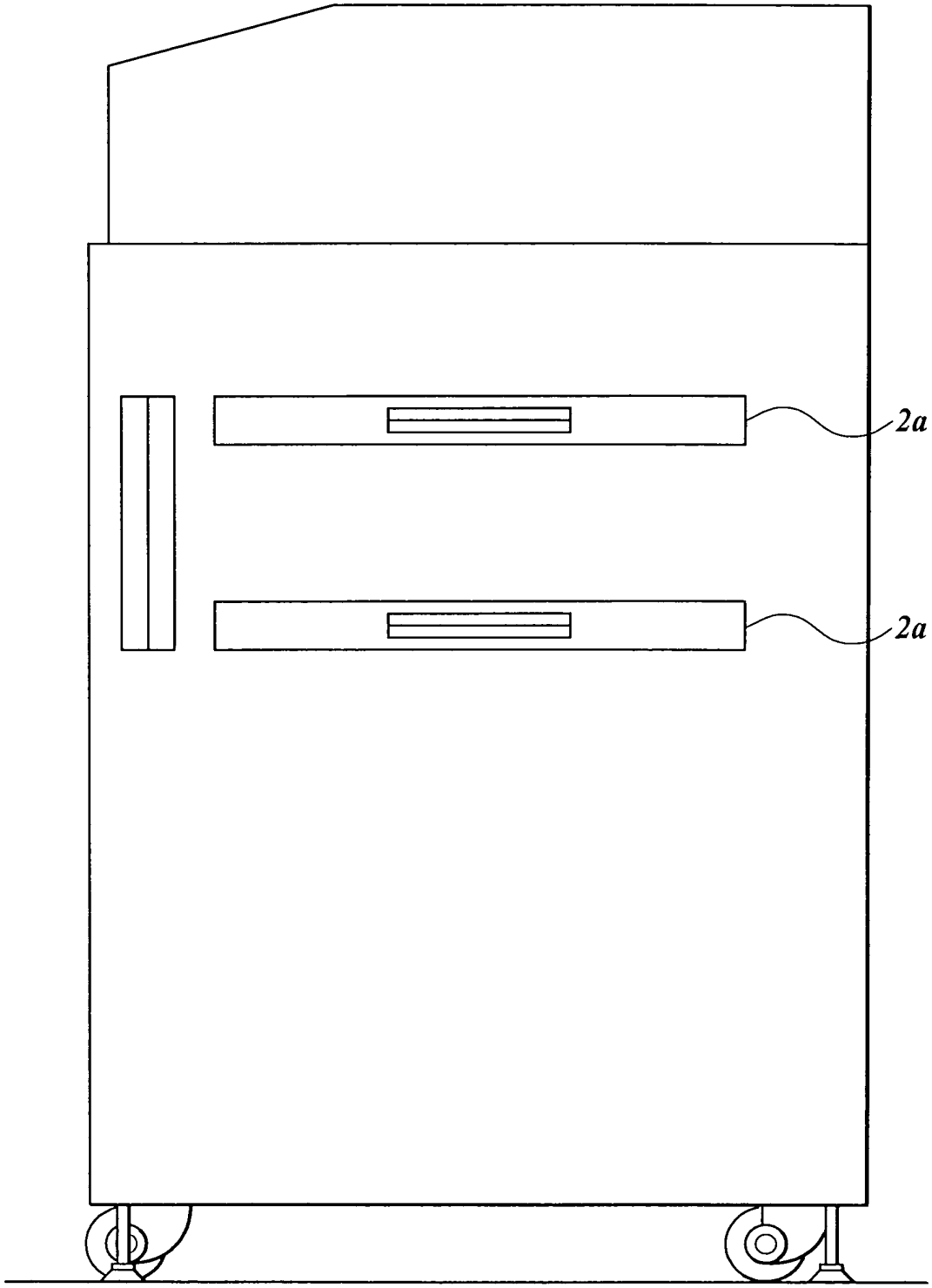
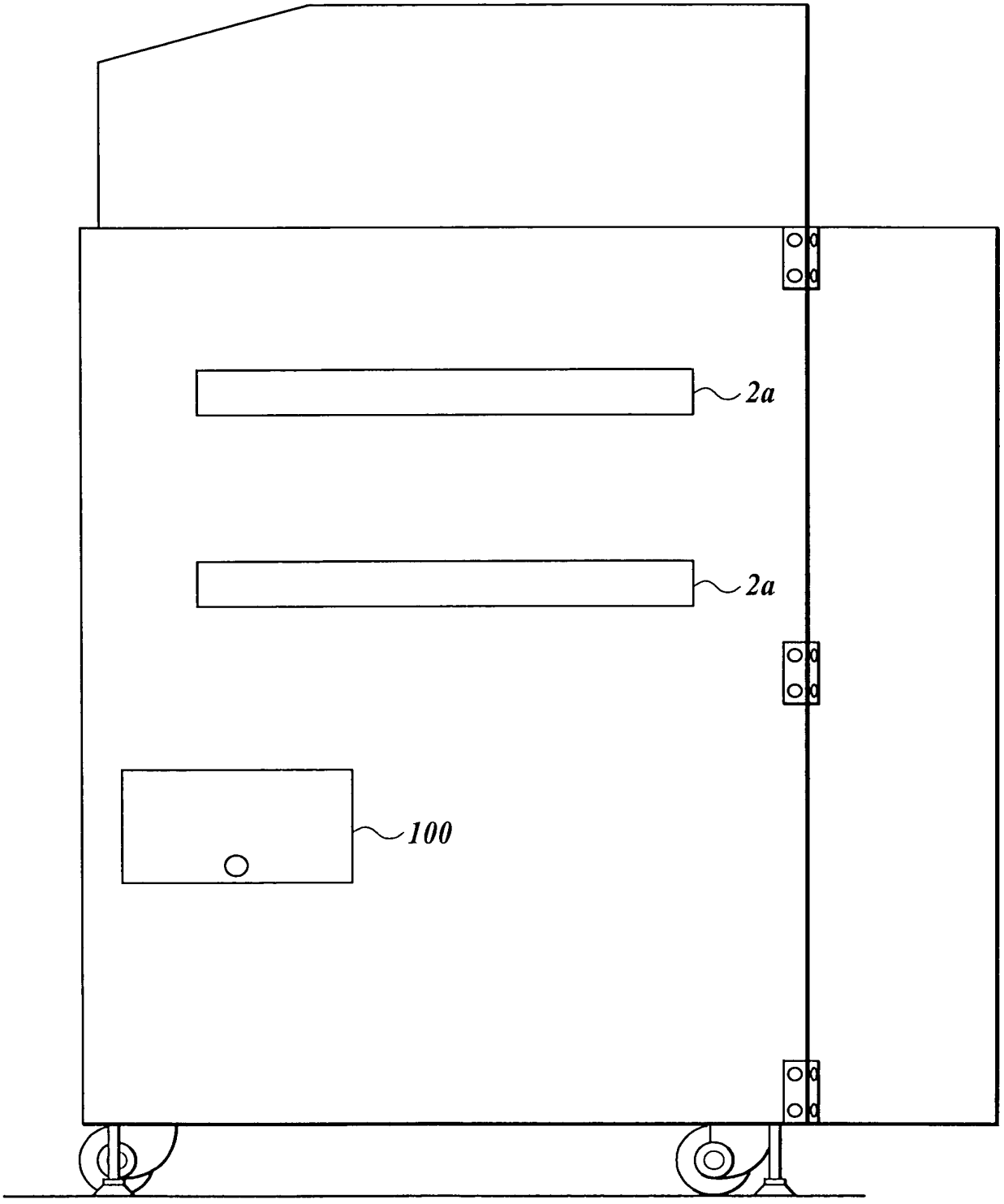
FIG.4